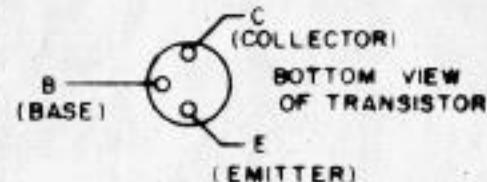


EC 638
303638

503638

EC640				303640			
+	+	+	+	+	+	1	CR31
+	2	+	+	+	+	2	CR25
+	3	+	+	+	+	3	CR26
+	4	+	+	+	+	4	CR27
+	5	+	+	+	+	5	+
+	6	+	CR9	+	+	6	+
CR1	7	CR10	+	CR17	7	CR28	+
CR2	8	CR11	+	CR18	8	CR29	+
CR3	9	CR12	+	CR19	9	CR30	+
CR4	10	CR13	+	CR20	10	+	+
CR5	11	CR14	+	CR21	11	+	+
CR6	12	CR15	+	CR22	12	+	+
CR7	13	CR16	+	CR23	13	+	+
CR8	14	+	+	CR24	14	+	+
+	15	+	+	+	15	+	+
+	16	+	+	+	16	+	+
+	17	+	+	+	17	+	+

NOTE REFER TO 6050 WD FOR BASIC MARKING INFORMATION



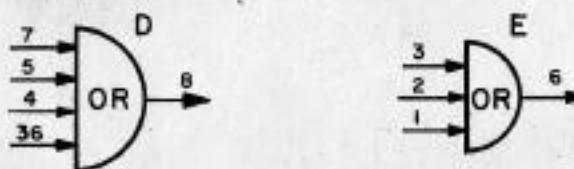
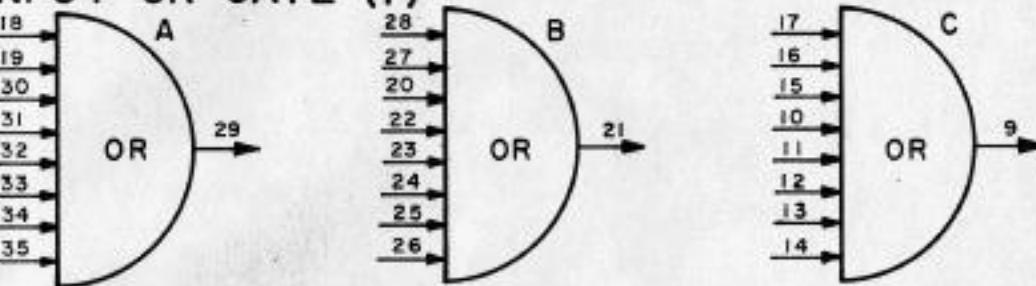
FIVE "OR" GATES ARE PROVIDED. THREE HAVE 8 INPUTS, ONE HAS 3 INPUTS AND ONE HAS 4 INPUTS. ALL GATES FUNCTION IDENTICALLY. A ZERO VOLT LEVEL ON ANY INPUT RESULTS IN A ZERO VOLT OUTPUT FROM THE CORRESPONDING GATE. WHEN ALL INPUTS TO A PARTICULAR GATE ARE AT -6 VOLTS, THE CORRESPONDING OUTPUT IS -6 VOLTS. EXTERNAL BIAS RESISTOR TO -12V. IS REQUIRED.

8 INPUT OR GATE (3)
3 INPUT OR GATE (1)
4 INPUT OR GATE (1)

CIRCUIT BOARD EC638

SYMBOL

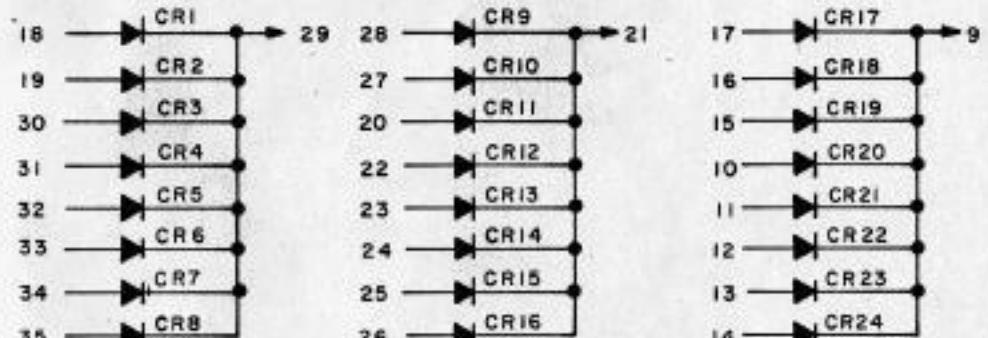
REVISIONS



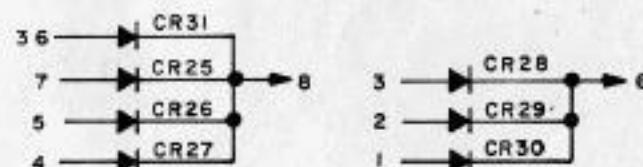
NOTE 2

WHEN TWO OR MORE GATES ARE TIED TOGETHER ONE SYMBOL CAN BE USED SHOWING PROPER TERMINAL DESIGNATIONS.

A B



D E



ALL DIODES ARE D2

